



22783 U.S. PTO

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

17510 U.S. PTO  
10/727262  
120303

In re Patent Application of: Kevin W. Glass et al.  
Title: BIASED DARLINGTON TRANSISTOR PAIR, METHOD, AND SYSTEM  
Attorney Docket No.: 80107.099US1

**PATENT APPLICATION TRANSMITTAL**

**MAIL STOP PATENT APPLICATION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items and information (as indicated with an "X"):

- ☒ Return postcard.
- ☒ Utility Patent Application under 37 CFR 1.53(b) comprising:
  - ☒ Specification (17 pgs, including claims numbered 1 through 30 and a 1 page Abstract).
  - ☒ Formal Drawing(s) (6 sheets).
  - ☒ Signed Combined Declaration and Power of Attorney (4 pgs).
  - ☒ Check in the amount of \$1036.00 to pay the filing fee.
- ☒ Assignment of the invention to Intel Corporation (2 pgs) and Recordation Form Cover Sheet.
- ☒ Check in the amount of \$40.00 to pay the Assignment recording fee.

The filing fee has been calculated below as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	30-20	10	x 18.00 =	\$180.00
INDEPENDENT CLAIMS	4-3	1	x 86.00 =	\$86.00
[] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
BASIC FEE				\$770.00
TOTAL				\$1036.00

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